HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 1 of 11

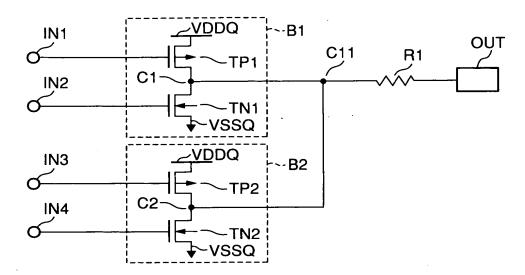
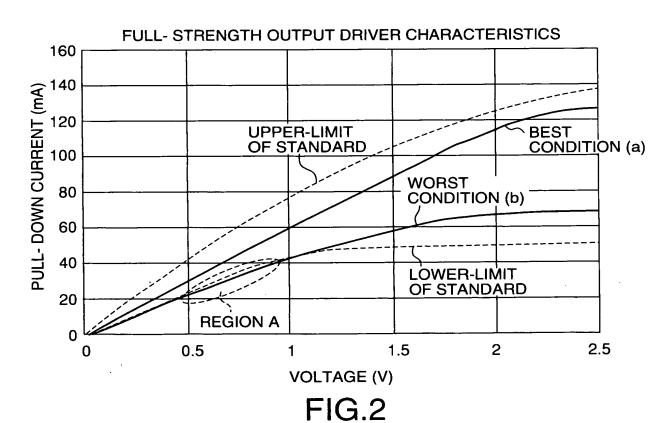


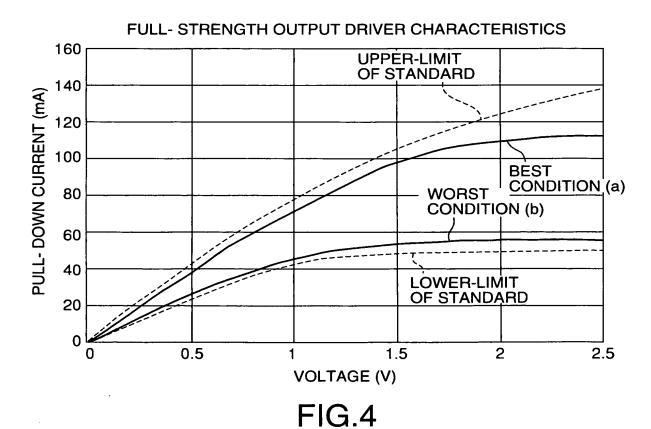
FIG.1



HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 2 of 11

IN1 **VD**DQ -B1 OUT C11 R11 TP1 C1 IN2 -TN1 -VSSQ /DDQ --B2 IN3 R12 TP2 IN4 -TN2 ·VSSQ

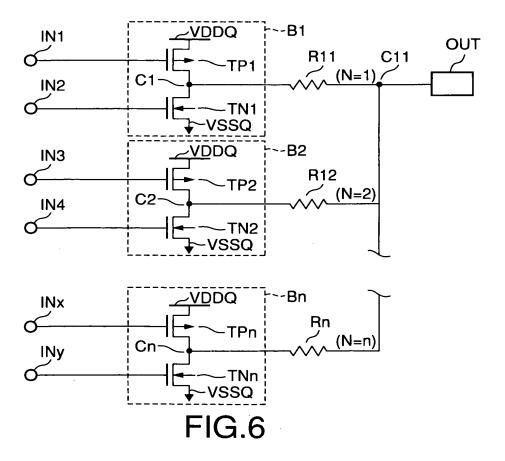
FIG.3



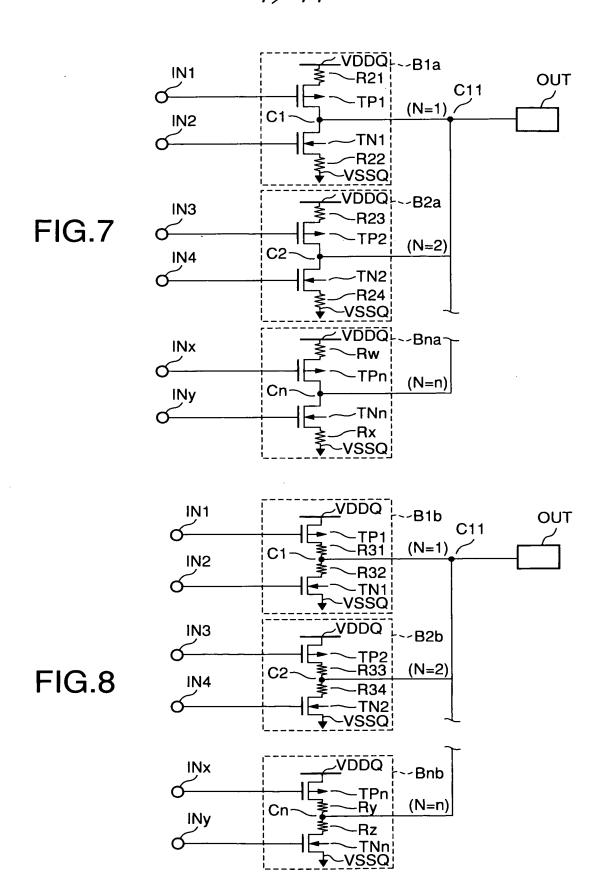
HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 3 of 11

IN1 <u>VD</u>DQ OUT C11 R11 TP1 IN2 C1--TN11 -TN12 -VSSQ **VDDQ** ~-B2 IN4 R12 TP2 C2-IN5 -TN21 -TN22 VSSQ

FIG.5



HOGAN & HARTSON - 81788.0261 Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same" Express Mail Label EV 325 214 875 US 11 Drawing Sheets; Sheet 4 of 11



HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor
Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 5 of 11

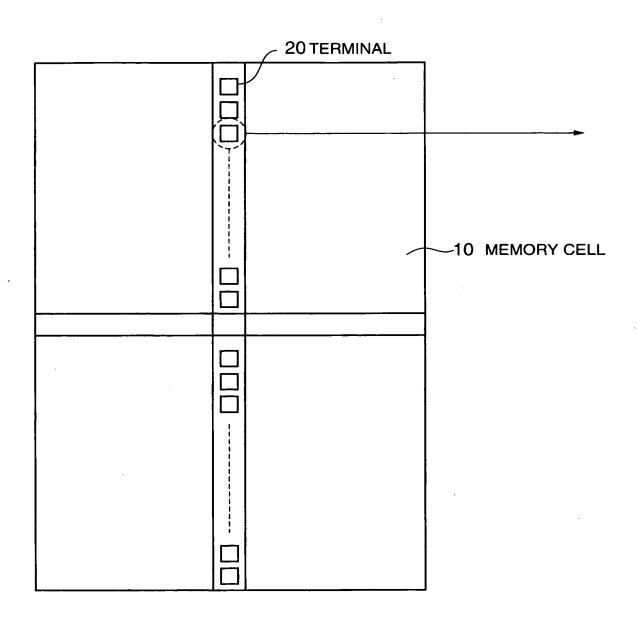


FIG.9

## HOGAN & HARTSON - 81788.0261 Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same" Express Mail Label EV 325 214 875 US 11 Drawing Sheets; Sheet 6 of 11

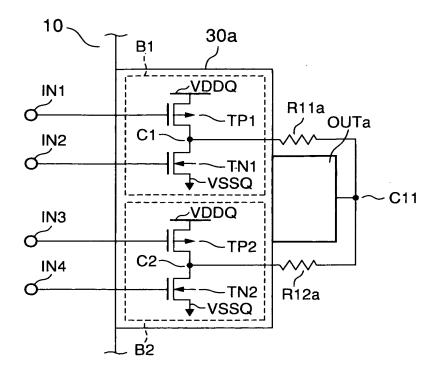
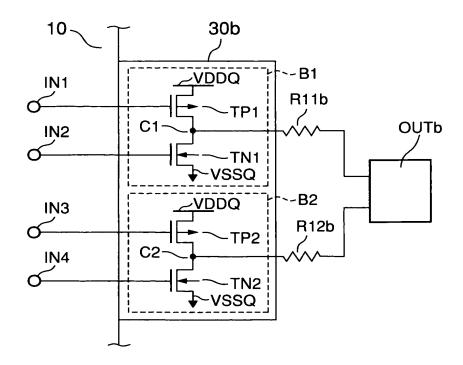
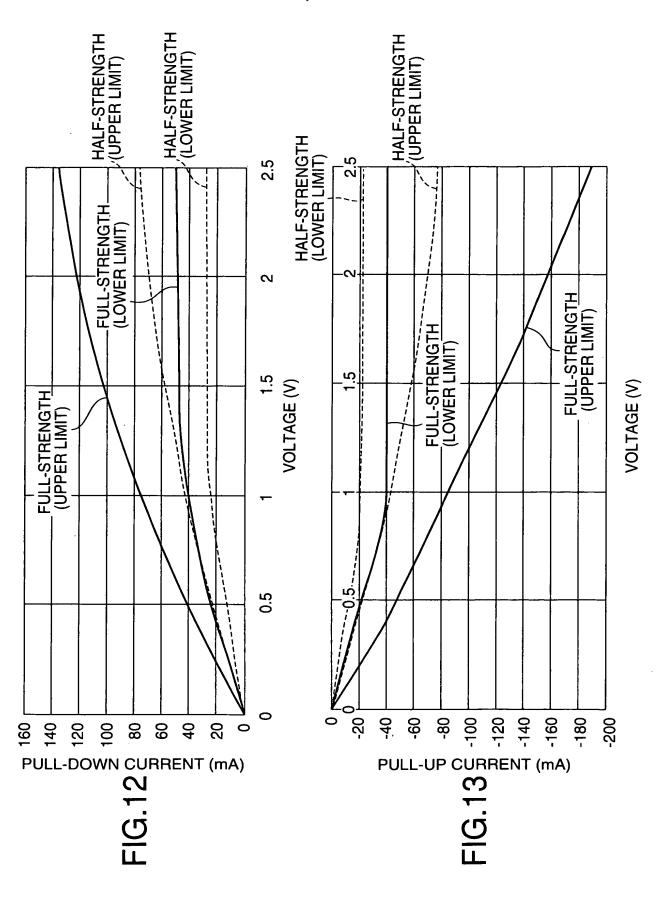


FIG.10



**FIG.11** 

HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor
Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 7 of 11



HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 8 of 11

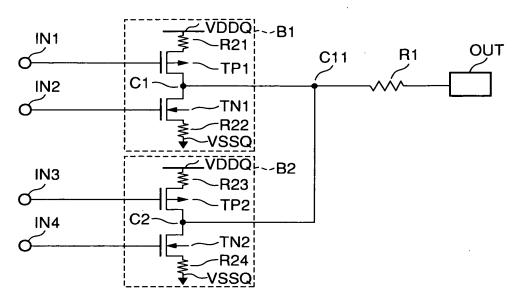


FIG.14

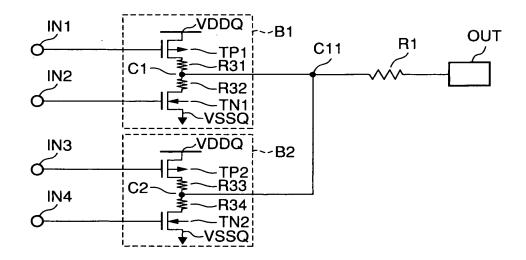


FIG.15

HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 9 of 11

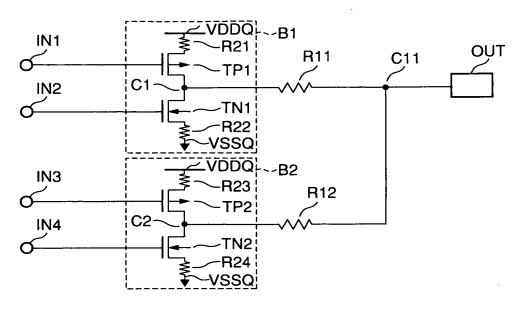


FIG.16

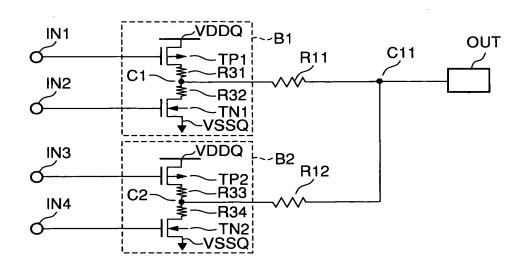
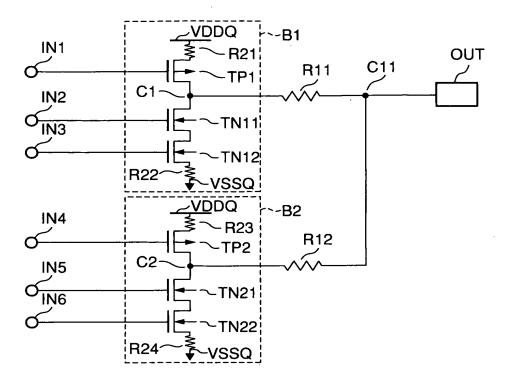
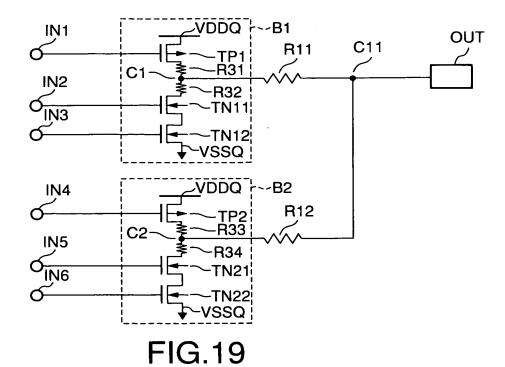


FIG.17

HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 10 of 11



**FIG.18** 



HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 11 of 11

